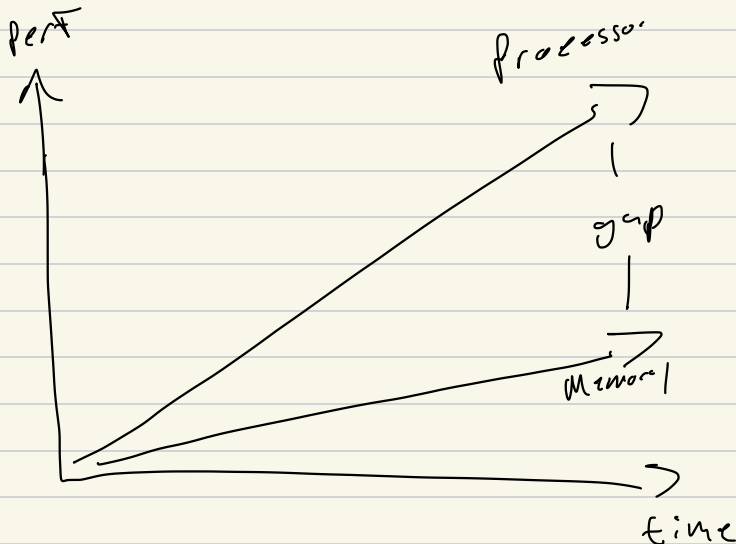
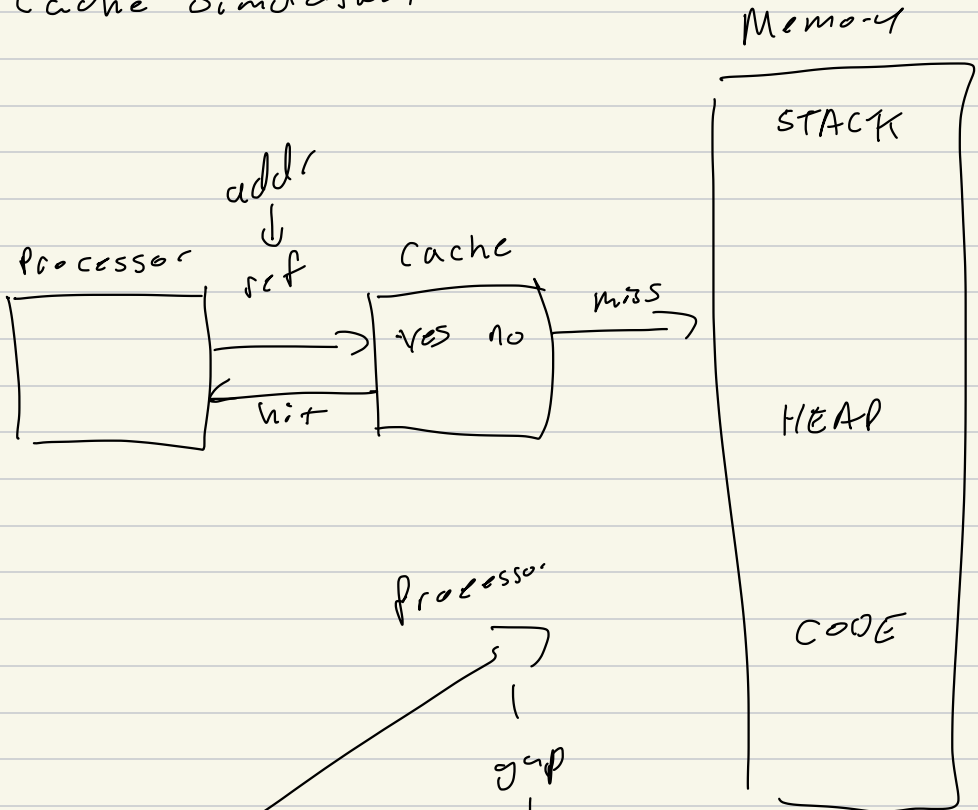


Project 03 due Thu Apr 2 11:59 pm

- 1) RISC-V Emulation
- 2) Dynamic Analysis
- 3) Cache Simulation



refs

hits

misses

$$\text{hit rate} = \frac{\# \text{ hits}}{\# \text{ refs}}$$

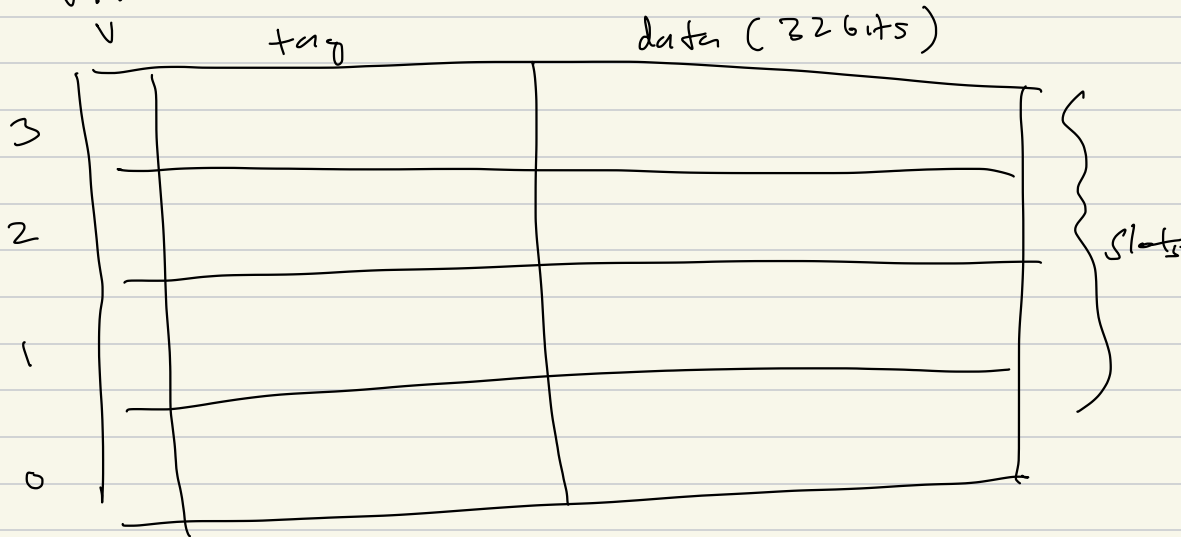
Cache
Questions ?

1) Where to look for a given address?

2) How to know if the addr/data is in the cache?

3) How to resolve a conflict?

Direct Mapped Cache

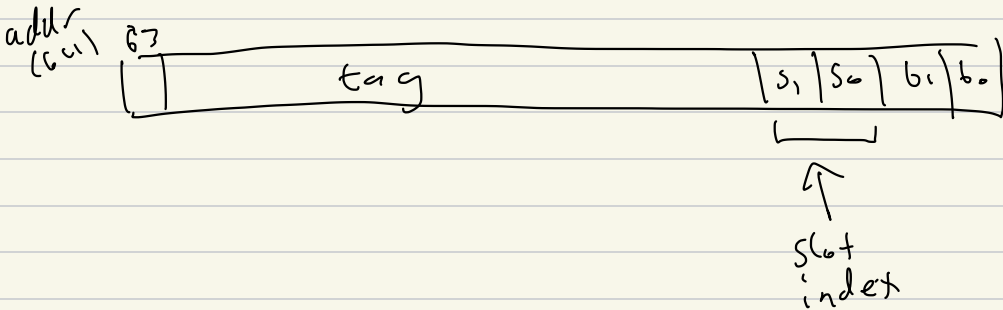


↑
slot
index

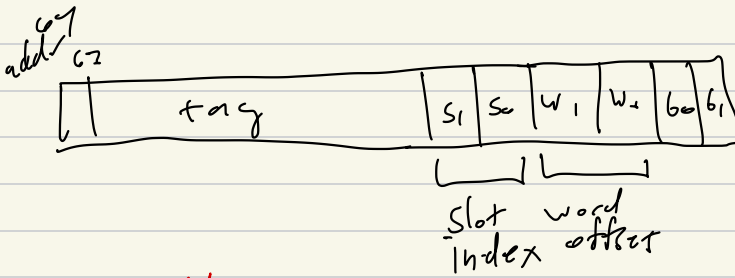
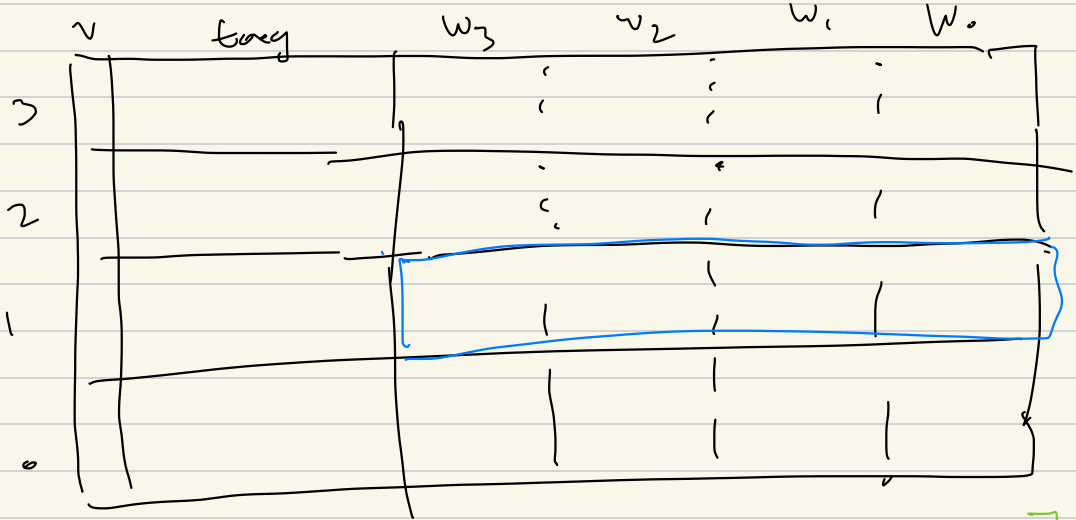
addr is word aligned

$$\text{addr_word} = \text{addr} / 4$$

$$\text{slot_index} = \text{addr_word} \% 4$$



Block size



addr →



